**NBKR INSTITUTE OF SCIENCE & TECHNOLOGY:: VIDYANAGAR**

**M.Tech (ECE) – I YEAR -I SEMESTER, Digital Electronics & Communication Systems**

SCHEME OF INSTRUCTION AND EVALUATION (With effect from the batch admitted in the academic year 2017-2018)

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| S.No | CourseCode | Course Title | InstructionHours/Week | Credits | Evaluation |
| SessionalTest-I | SessionalTest-II | Total Sessional Marks (Max. 40) | End SemesterExamination | MaxTotal Marks |
| **THEORY** | L | T | P/D |  | DurationIn Hours | Max.Marks | DurationIn Hours | Max.Marks | 0.8\*Best of 2 mids + 0.2\*other mid | DurationIn Hours | Max.Marks |  |
| 1 | 17EC1101 | Digital System Design | 4 | - | - | 4 | 2 | 40 | 2 | 40 | 3 | 60 | 100 |
| 2 | 17EC1102 | Advanced Digital Signal Processing  | 4 | - | - | 4 | 2 | 40 | 2 | 40 | 3 | 60 | 100 |
| 3 | 17EC1103 | Embedded System Concepts | 4 | - | - | 4 | 2 | 40 | 2 | 40 | 3 | 60 | 100 |
| 4 | 17EC1104 | Coding Theory and Techniques | 4 | - | - | 4 | 2 | 40 | 2 | 40 | 3 | 60 | 100 |
| 5 | 17EC1105 | Transform Techniques | 4 | - | - | 4 | 2 | 40 | 2 | 40 | 3 | 60 | 100 |
| 6 |  | Elective -I | 4 | - | - | 4 | 2 | 40 | 2 | 40 | 3 | 60 | 100 |
|  | **PRACTICALS** |  |  |  |  |  |
| 7 | 17EC11P1 | Digital System Design Lab | - | - | 3 | 2 | Day to Day Assessment | 40 | Day to Day Evaluation(30) + a test(10)=(40 Marks) | 3 | 60 | 100 |
| 8 | 17EC11P2 | Seminar-I | - | - | 4 | 2 | Periodical Review and Internal Seminar | 100 | Continuous Assessment (100) | - | - | 100 |
|  |  | **TOTAL** | **24** | **-** | **07** | **28** | **-** | **-** | **-** | **-** |  | **-** | **-** | **800** |